

**PRESILICON DISK MODEL FOR DESIGN, DEVELOPMENT AND VALIDATION**

**ABSTRACT OF THE DISCLOSURE**

A method for verifying the design of a disk controller circuit to be incorporated into a targeted hard disk drive system, the targeted hard disk drive system including a read/write channel and a head actuator. The method may include steps of emulating reading and writing of data in the read/write channel based upon a model of the read/write channel, emulating a behavior of the head actuator during track seek and track following operations based upon an electromechanical model of the head actuator, providing a disk controller design base for defining integrated circuit elements comprising the disk controller circuit and providing a controller environment to support execution and debug of firmware for operating the disk controller circuit. A plurality of disk functions may then be carried out according to a script running on a host. The plurality of disk functions includes interaction of at least the read/write model, the electromechanical model, the disk controller design base and the controller environment.